

**ADAPTIVE RESONANT SWITCHING POWER SYSTEM****Cross Reference To Related Application**

This Application Claims Priority To

U.S. Provisional Patent Application No. 60/437,584 Filed December 31, 2002

5

**Field of the Invention**

The present invention relates to power supply circuits and more particularly to a power converter having a first resonant frequency dependent upon leakage inductance of an output transformer and capacitor and a second resonant frequency dependent upon gate source capacitance of the circuit's power MOSFETs and leakage inductance of a driving transformer.

10

**Background of the Invention**

Power conversion devices known in the art such as resonant DC-to-DC power converters are used to provide power in audio applications and in a number of other electronic applications. A popular type of resonant power converters is the well known class D power converter.

15

Class D converters operate by alternatively turning on and off a pair of power MOSFETs (metal oxide semiconductor field effect transistors) in response to a square wave switching signal. Current from the MOSFETs passes through a resonant capacitor and primary windings of a high frequency transformer which efficiently converts the pulsed high voltage MOSFET output to a suitably lower output voltage. Output from the secondary coils of the high frequency transformer are rectified and filtered to provide smooth DC voltage of an appropriate magnitude for many electronic applications.

20

Resonant power converters have good thermal efficiency under ideal conditions because the switching signal to the MOSFETs is timed to turn each MOSFET off when the current through it is zero and the switching signal to the MOSFETs is timed to turn each MOSFET on when the voltage across it is zero. Under ideal conditions, each MOSFET is switched at a 50% duty cycle. However, in non-ideal conditions, the switching duty cycle is typically varied to provide voltage regulation as the input voltage varies. Variation in the duty cycle causes at least one of the MOSFETs to turn off when it carries non-zero current or turn on with non-zero voltage thereby introducing thermal losses.

30

U.S. Patent No. 5,986,895 to Stewart et al. discloses a method of maintaining zero current switching of MOSFETs under non-ideal conditions in a resonant power converter. This method senses the transformer primary current and the output voltage and uses digital logic to modify the MOSFET switching signal. The digital logic is designed so that the modified switching signal switches the MOSFETs at zero current even under non-ideal conditions such as asymmetric duty cycles.

Digital "brute force" methods such as the method disclosed in the Stewart et al. Patent, require a number of design compromises including increased real estate consumption on a board or chip. Also, significant additional design time and complexity is added to component layout in order to place such a digital system in close proximity to the high frequency switching components in a resonant power converter. Each of these disadvantages adds significant cost to any power converter using this technique which can make their use prohibitively expensive for many applications.

### Summary of the Invention

The adaptive resonant switching power system according to an illustrative embodiment of the present invention operates with two resonant frequencies. A first frequency depends on leakage inductance of an output transformer and capacitor. The first frequency varies with changes in the load because load changes alter the leakage inductance. In an illustrative embodiment of the invention, the first frequency is used as the power converter's switching frequency when the load current is above approximately 10% of the converter's maximum load magnitude.

A second resonant frequency depends on the gate source capacitance of two MOSFET power devices in the circuit and the leakage inductance of the circuit's driving transformer. In the illustrative embodiment, the second resonant frequency is used as the power converter's switching frequency when the load falls below approximately 10% of the full load magnitude.

The driving transformer includes a pair of drive coils and a sense coil. The sense coil senses the load current in the output transformer secondary circuit. The drive coils are arranged to drive a pair of MOSFETs 180 degrees out of phase. When operating in the load dependent region, (over 10% of maximum load magnitude), the phase reversal of the load

current causes phase reversal of the drive to the MOSFETs so that zero current switching, and zero voltage switching, with no cross conduction is always maintained regardless of the particular load reactance. In the illustrative embodiment, the gate-source capacitance becomes part of the oscillator for the base frequency, (under about 10% of maximum load magnitude). The specially designed current sensing transformer according to the present invention drives the two power MOSFETs so that a gate charge oscillates between the two MOSFETs (from one to the other), The MOSFETs thereby always operate even under no load conditions, i.e., when there is no secondary load current. The invention thereby minimizes the power dissipation and allows the design of a very small and efficient switching power transformer.

The present invention features a power converter having a first resonant frequency that depends on the reactance of a load. Changes in the resonant frequency of the power converter according to the invention occur according to changes in load capacitance or inductance such that the frequency is inversely proportional to the square root of the product of inductance and capacitance. Further, the core material of the output transformer is chosen such that its relative permeability decreases with increasing load current, lowering the leakage inductance and raising the frequency. External capacitance and inductance can be chosen to set the initial frequency as required.

Because the power converter will always operate at its highest resonant frequency, it is important that the load dependent frequency be higher than the base frequency determined by the MOSFET gate source capacitance and the inductance of the driving transformer.

The invention thereby features a switching power converter that switches at times of zero load current. Since no current flows during the switching transient, power (IR) losses are extremely low. The present invention features efficiency of 90% or greater and operates without a minimum load.

An illustrative embodiment of the invention includes an adaptive resonant power converter having a gate drive transformer. The gate drive transformer includes a first sensing primary winding, a first secondary winding, and a second secondary winding. The illustrative embodiment also includes a first MOSFET having a gate connected to the first secondary winding and a second MOSFET having a gate connected to the second

secondary winding. The first and second MOSFETS are connected to each other in series across a direct current input source and form a common MOSFET node therebetween at the first MOSFET source and the second MOSFET drain. An input capacitor is connected across the direct current input source. The input capacitor has a first capacitor node  
5 connected to the first MOSFET drain and a second capacitor node connected to the second MOSFET source.

The illustrative embodiment also includes a main transformer having a primary winding and a first resonant output winding. The main transformer primary winding is connected between the common MOSFET node and the second capacitor node. A first tap  
10 of the first resonant output winding is connected to a first tap of the first sensing primary winding. A second tap of the first resonant output winding is connected to a first load terminal of a first load. A second tap of the first sensing primary winding is connected to a second load terminal of the first load.

In an illustrative embodiment, the gate drive transformer also includes a protection  
15 diode winding connected in parallel with a pair of parallel voltage limiting diodes, the voltage limiting diodes having opposite orientations. A compensation capacitor can be connected between the gate and source of each MOSFET to provide resonant oscillation with the first secondary winding and the second secondary winding. A damping resistor can be connected in series between each of the MOSFET gates and its corresponding  
20 secondary winding on the gate drive transformer.

Because of its high efficiency, the present invention also features low heat dissipation and thereby requires small heat sinks. Accordingly, devices incorporating the power converter according to the present invention can be designed with smaller and less expensive packaging for a given power rating.

25 The present invention is also capable of very low radio frequency interference (RFI) emissions because of the zero current switching and features smaller transformers that are less bulky and lighter than transformers used in prior art switching power converters.

The present invention also minimizes the need for high frequency circuit board layouts that are difficult to manufacture. For example, the circuit board layout in the  
30 present invention is less critical than non resonant designs and the composition of circuit

boards that can be used in the present invention is not critical. Manufacturing costs are thereby reduced due to ease of manufacturing.

The present invention also features advantages for use in audio applications because the switching power converter according to the invention has fast dynamic response due to  
5 changes in the resonant frequency as a function of the load.

### **Brief Description of the Drawings**

The foregoing and other features and advantages of the present invention will be more fully understood from the following detailed description of illustrative embodiments  
10 taken in conjunction with the accompanying drawings in which:

FIG. 1 is a schematic circuit diagram of a resonant power system driving an alternating current (hereinafter "AC") load according to an illustrative embodiment of the present invention;

FIG. 2 is a schematic circuit diagram of a resonant power system driving a direct  
15 current (hereinafter "DC") load according to an alternative embodiment of the present invention;

FIGS 3 – 5 are schematic circuit diagrams of a resonant power system driving one or more additional loads according to alternative embodiments of the present invention;

FIG. 6 is a schematic circuit diagram of a resonant power system having a single  
20 input capacitor according to an alternative embodiment of the present invention; and

FIG. 7 is a schematic circuit diagram of a resonant power system including optional compensation capacitors and damping resistors according to alternative embodiments of the present invention.

### Detailed Description

An illustrative embodiment of the present invention is described with reference to FIG. 1. A gate drive transformer 14 includes a core 22, primary coil 24, two secondary coils 20, 18, and an auxiliary coil 7. The primary coil 24 is connected between the load and the secondary of transformer 36. The first secondary coil 20 is connected between the gate and source of first MOSFET 10, the secondary coil 18 is connected between the gate and source of second MOSFET 12. The auxiliary coil 7 is connected across a pair of rectifier diodes 80, 90. A rectified power source from an AC input 32 through a bridge rectifier 34 is connected between the source of second MOSFET 12 and the drain of first MOSFET 10. A pair of capacitors 30, 28 are also connected in series with each other and in parallel with the MOSFETs across the rectified power input. The source of the first MOSFET 10 is connected to the drain of the second MOSFET 12 at a common node 38. A sensor coil 24 is included in the primary windings of the gate drive transformer 14. The sensor coil 24 is in series with the load 26. The sensor coil 24 and load 26 are connected across the secondary windings of the second transformer 36. One tap of the primary winding of the second transformer 36 is connected to the common node 38. The other tap of the primary winding of the second transformer 36 is connected to a node between the two capacitors 30, 28.

In the illustrative embodiment, the resonant switching power converter according to the invention uses a varied frequency. The inherent gate source capacitances of the power MOSFETs 10, 12 are used, in conjunction with the inductance formed by secondary coils 20 and 18, as an oscillating element. One resonant frequency, hereinafter referred to as the base frequency, depends only upon the leakage inductance of the gate drive transformer 14 and the inherent gate-source capacitance of the power MOSFETs 10, 12. Those skilled in the art will recognize that the frequency of this oscillator is inversely proportional to the square root of the product of twice the individual drive coil inductance and the MOSFET gate-source capacitance according to the relation  $f = 1/\sqrt{4L*1/2C}$  where "f" is the oscillator frequency in radians per second, "L" is the drive coil inductance in Henrys and "C" is the MOSFET gate-source capacitance in Farads.

Another resonant frequency, hereinafter referred to as the load frequency, depends only upon the equivalent circuit resonant frequency of the load and the transformer 36, secondary current. In the illustrative embodiment, the power converter according to the

invention always operates on the higher of the two frequencies. For example, when the load current falls below a predetermined limit, the power converter switches at the base frequency. When the load frequency is above the preselected limit, the power supply switches at the load frequency, which is higher than the base frequency. In normal operation, the power supply is switching at the load frequency most of the time and switching in phase with the load current thereby minimizing thermal losses and maximizing efficiency. When the circuit is operating on the load frequency, the gate drive transformer 14 drives the MOSFETs, driven by the secondary current in main transformer 36 and switching at the zero crossing. When the power supply is switching at the base frequency, the gate drive transformer 14 also drives the MOSFETs 10, 12.

The gate drive transformer 14 is specially designed to force the two power MOSFETs 10, 12 to drive each other out of phase. The resonance between the two power MOSFETs creates ideal working conditions requiring very low driving power. This minimizes power dissipation in the power supply, provides very high efficiency and allows the power converter to operate with no load current.

For proper operation it is important that the core material for the gate drive transformer 14 be selected such that the base frequency formed by the leakage inductance and the MOSFET 10,12 input capacitance be lower than the load frequency formed by the secondary leakage inductance of main transformer 36 and the load reactance. Further, the core material chosen for the main transformer 36 should provide characteristics such that the secondary leakage inductance decreases with higher flux density. That is, the load frequency will increase with increasing load current. The characteristics of both circuits should be chosen such that the base frequency and load frequency are the same at approximately 10% of the maximum load magnitude to facilitate a smooth crossover between operating modes.

The load frequency of the switching power supply according to the invention is also maintained by responding to changes in reactance of the load 26. For example, when the load becomes more inductive or capacitive, the switching frequency (load frequency) of the power supply decreases but maintains the zero current switching feature. By keeping the power supply switching frequency in phase with the load frequency, the load voltage is always in phase with the load current. Power dissipation at the main transformer 36 core and the power MOSFETs 10, 12 is thereby minimized. This allows reductions in the size of

main transformers and the heat sinks of the switching power supplies according to the invention.

The auxiliary coil 7 is used in combination with rectifier diodes 80, 90 to limit the voltage that can be generated by secondary coils 18, 20. This provides protection by limiting the voltage applied across the gate and source of MOSFETs 10, 12. This feature can eliminate the need of other more expensive means for protecting the MOSFET gates from voltage breakdown.

A starting circuit 15 is required to start the base frequency oscillation between the two MOSFETs. Starting circuits are well known. A starting circuit 15 as depicted in FIG. 1 can easily be configured by persons having skill in the art. Although a starting circuit 15 is required in each of the embodiments described herein, the starting circuit it is not shown in FIGS. 2 – 7 for clarity.

The illustrative embodiment of the invention described hereinbefore is configured to provide an AC output. Those skilled in the art will appreciate that all the advantages of the converter can be maintained for a rectified DC output by constructing an alternative embodiment. One such alternative embodiment is shown in FIG. 2 wherein a first rectifier diode 39 and an appropriate filter (not shown) is placed between the secondary of output transformer 36 and the load, a capacitive storage element 40 is placed at the output of the diodes, and a second output circuit is added. In the second output circuit, a second output winding 48 having the same number of turns as the first output winding 47 is added to main transformer 36 and a second sense coil 42 having the same number of turns as the first sense coil 24 is added to gate drive transformer 14. The second output circuit is connected with its own output winding 48 and sense coil 42 in a configuration like the first output circuit except having an opposite polarity of sense coil 42 and output winding 48. The second rectifier diode 41 is connected between the first rectifier diode 39 at capacitor 40 and the second output winding 48 of transformer 36 such that the first 39 and second 41 rectifier diodes are oriented in the same direction relative to the load 26. The first 39 and second 41 rectifier diodes thereby conduct on opposite phases of the driving signal to main transformer 36. The first 24 and second 42 sense coils are connected to the opposite end of the capacitive storage element 40 in the filter from the end where the first 39 and second 41 rectifier diodes are connected in common. The polarity of the connection of the second

sense coil 42 of gate drive transformer 14 is such that the proper alternating drive to MOSFETs 10 and 12 is maintained.

Another embodiment of the invention is shown in FIG. 3 in which one or more auxiliary load coils 100 can be added the main transformer 36 to provide power to one or more additional loads 102. In this embodiment, a first additional gate drive coil 104 is added to the main transformer and connected through a resistor 106 to a second additional gate drive coil 108 in the first transformer 14.

The one or more auxiliary load coils 100 in the main transformer 36 provide additional power taps or voltages from the transformer 36. Such additional power taps are commonly desirable in the industry. Additional loads can thereby be connected to the main transformer in parallel to the main load 26 that drives the MOSFET switching control.

The first and second additional gate drive coils 104, 108 are used in this embodiment to efficiently drive additional loads that are connected to drive the MOSFET gates when the main output is unloaded, i.e. when zero current flows through load 26. Even though the converter will operate on the gate oscillator frequency when the main output is unloaded as previously described, the relatively small parasitic energy oscillating between the MOSFETs may not transition the MOSFETs quickly enough to provide efficient switching of power to the additional loads.

In such an embodiment, efficiency is improved by adding an auxiliary coil to both the main transformer and the gate drive transformer. The function of this coil is to provide additional energy and feedback to the gate drive circuit to increase the speed of transition between states of the MOSFETs. Peak efficiency is thereby achieved because the MOSFETs are driven with enough energy to transition quickly and spend maximum time in the saturated operating region (either fully on or fully off). This avoids thermal losses that could otherwise result from slow transitions when the MOSFETs are driving an auxiliary load.

In one embodiment, as shown in FIG 4, additional gate drive coils 104 and 108 and resistor 106 are provided to replace (rather than supplement) the primary load 26 and first sense coil 24. In this embodiment, one or more loads (Zn) 110 can be added in parallel using a corresponding number of coils (Ln) 112 that tap into the main transformer 36. Any

or all of these loads 110 can be switched open without causing thermal losses because the additional gate drive coils 104, 108 are always providing feedback to the gate drive transformer 14.

5 As shown in FIG. 5, additional loads could also be efficiently driven according to the present invention without providing additional gate drive coils if the primary load 26 is required to remain switched into the circuit at all times. If such a restriction is maintained, the additional loads 102 can be switched in or out of the circuit and/or draw varying or zero current without causing thermal losses in the MOSFETS 10, 12.

10 Although the embodiment shown in FIGS. 3-5 are described in terms of a circuit for providing AC power to a load, persons skilled in the art should appreciate that additional gate drive coils 104, 108 and resistor 106 can be added in the same manner to allow efficient operation of additional loads in an DC power circuit according to the present invention such as the circuit shown in FIG. 2.

15 In certain embodiments of the present invention as shown in FIG. 6, only a single capacitor 30 is connected between the input rectifier bridge 34 and primary winding of the main transformer 36. For example, in applications wherein the present invention provides power in ranges up to from about 700W to about 1000W a single capacitor 30 connected between the input rectifier bridge 34 and the primary winding of the main transformer 36 as shown is sufficient. Persons skilled in the art should appreciate that a large single capacitor  
20 could also be used at higher power levels, however the size of such capacitors usually makes their use impractical.

It will typically be advantageous to use one type of gate drive transformer 14 with various types of MOSFETs 10, 12 that are selected according to the power requirements of a particular application. Rather than designing a new gate drive transformer 14 with a  
25 leakage inductance to match the gate capacitance of each particular pair of MOSFETs, persons skilled in the art should appreciate that it is less costly to install compensation capacitors 114, 116 as shown in FIG. 7 between the gate and source of the MOSFETS 10, 12 such that the compensated gate/source capacitance is tuned to the leakage inductance of the gate drive transformer as described hereinbefore.

In still another embodiment of the present invention, damping resistors 118, 120 can be connected between the gate of the MOSFETS 10, 12 and the gate drive transformer 14 as shown in FIG. 7 to damp spurious oscillations as known in the art. Such resistors are typically in the range of from about less than one ohm to about greater than two ohms.

5           Although the embodiments shown in FIGS. 6-7 are described in terms of a circuit for providing AC power to a load, persons skilled in the art should appreciate that a single input capacitor 30 as shown in FIG 6, compensation capacitors 116, 114 and low value resistors 118, 120 as shown in FIG 7 can be added in the same manner to provide design and manufacturing flexibility in an DC power circuit according to the present invention  
10       such as the circuit shown in FIG. 2.

Although the invention has been shown and described with respect to exemplary embodiments thereof, various other changes, omissions and additions in the form and detail thereof may be made therein without departing from the spirit and scope of the invention.